

00322T-26264250

106

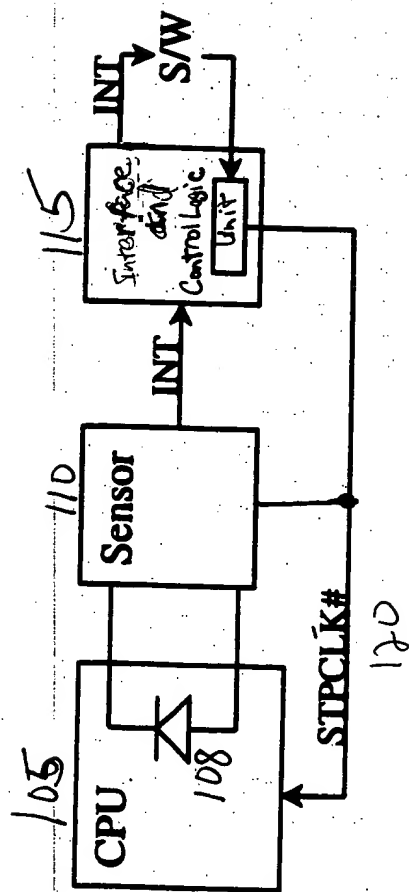
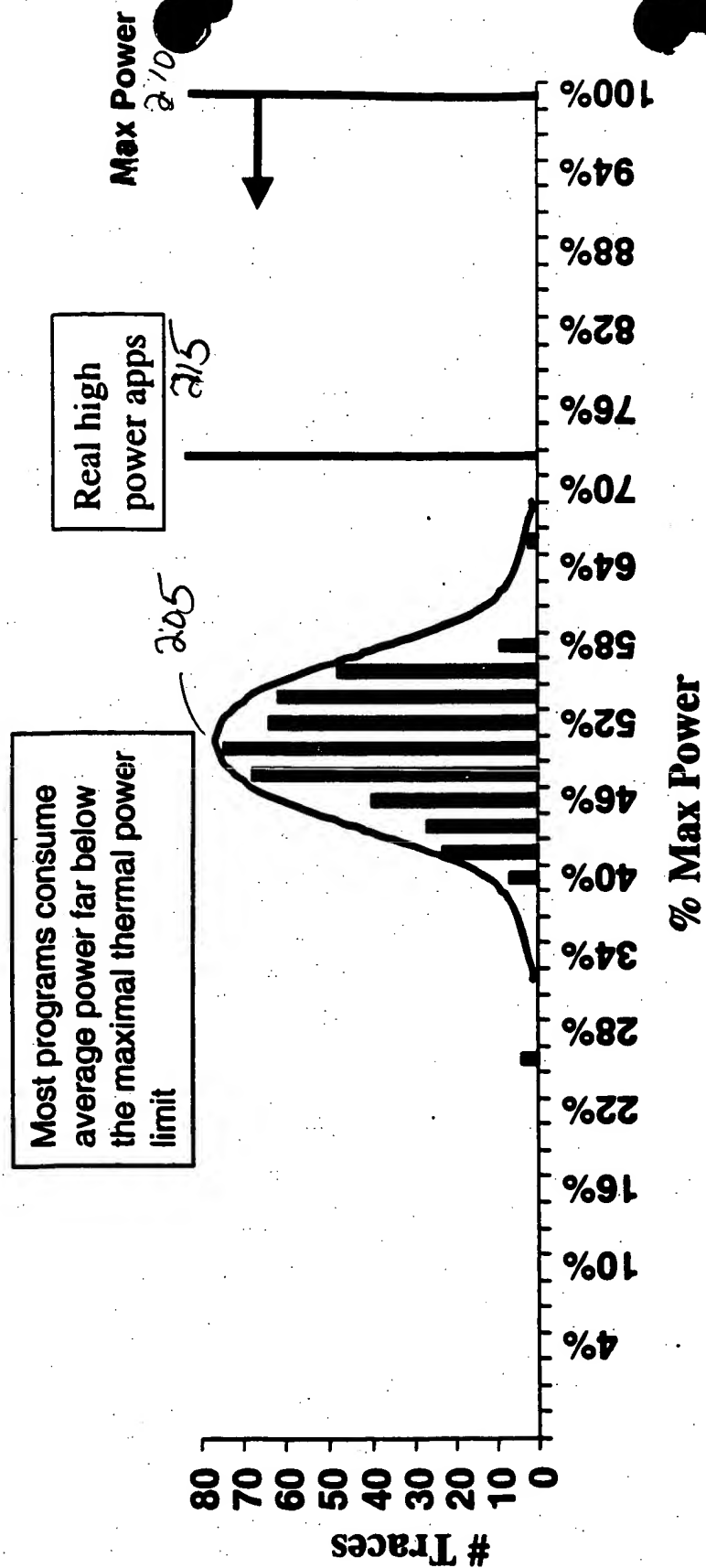


FIG. 1

7006

1000



256

# S E C R E T



FIG. 3

008221" 26254260

400

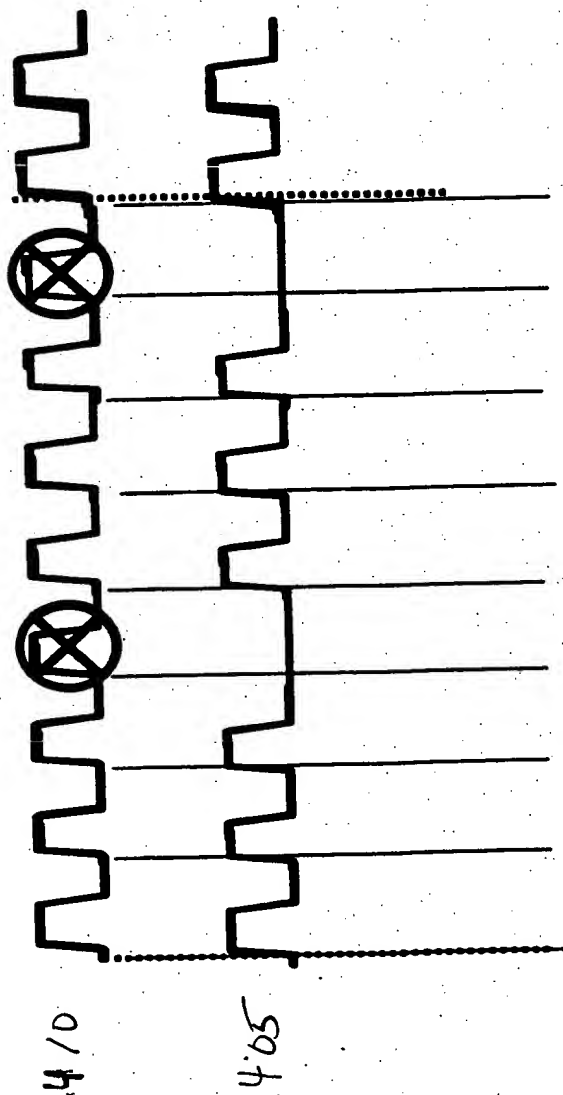


Figure 4

500

FFRL_EN	dT/dt	Thermal temperature	Current logic state	Prev. logic state
0 (not near maximal thermal limit)	Not Care	Not Care	Power down	Power down
0 (not near maximal thermal limit)	Not Care	Not Care	Power down	Wait
0 (not near maximal thermal limit)	Not Care	Not Care	Power down	Active
1 (near maximal thermal limit)	<0.2 (slow rate)	<max. temperature - $\delta t$	Power down	Power down
1 (near maximal thermal limit)	>0.2 (slow rate)	<max. temperature - $\delta t$	Wait	Power down
1 (near maximal thermal limit)	<0.2 (slow rate)	<max. temperature - $\delta t$	Power down	Wait
1 (near maximal thermal limit)	>0.2 (slow rate)	<max. temperature - $\delta t$	Wait	Wait
1 (near maximal thermal limit)	Not Care	>max. temperature - $\delta t$	Active	Power down
1 (near maximal thermal limit)	Not Care	>max. temperature - $\delta t$	Active	Wait
1 (near maximal thermal limit)	Not Care	>max. temperature - $\delta t$	Active	Active

FIG 5.

00000000

609



## Figure 6